

PRIOR ART

FIG. 1

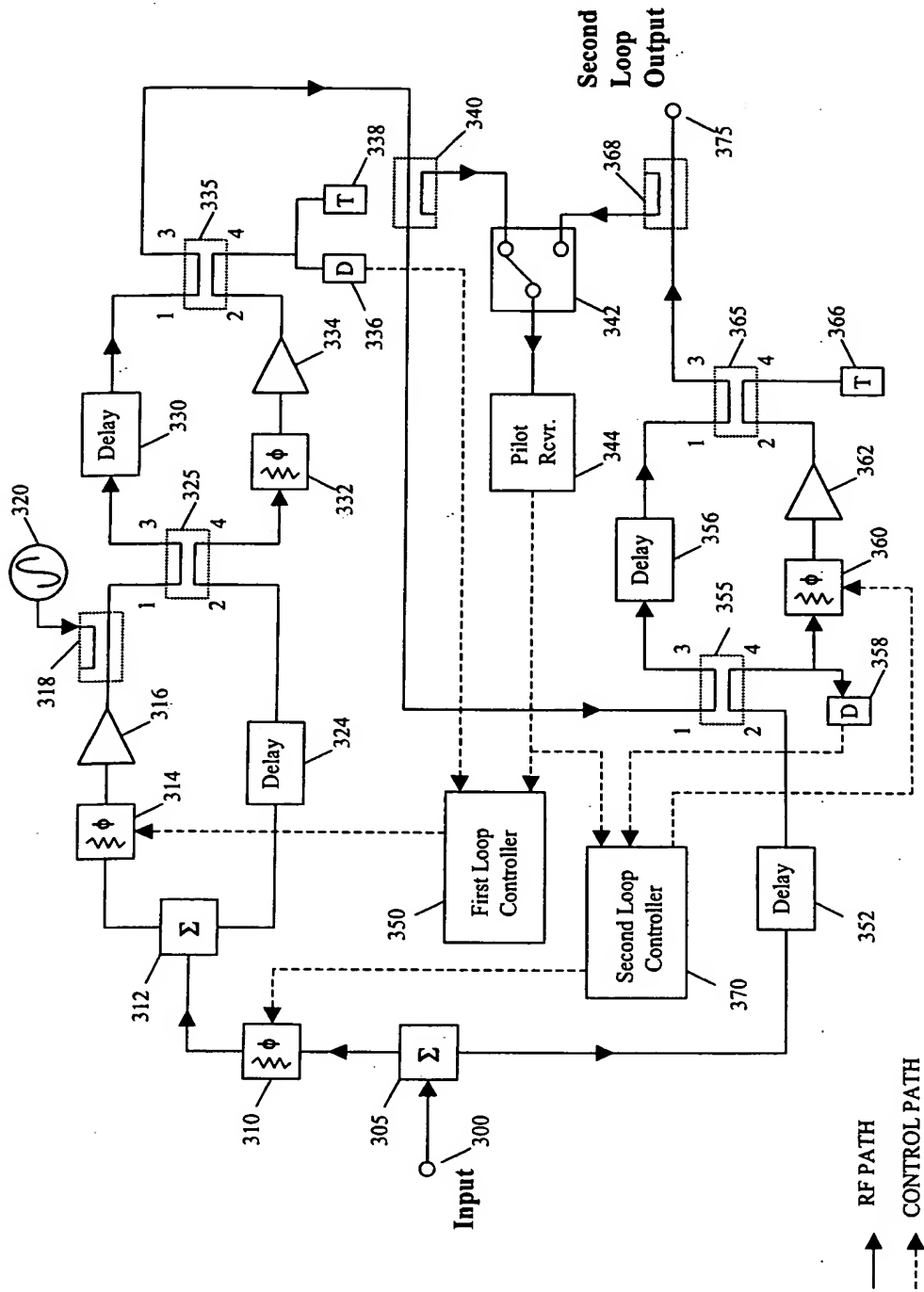
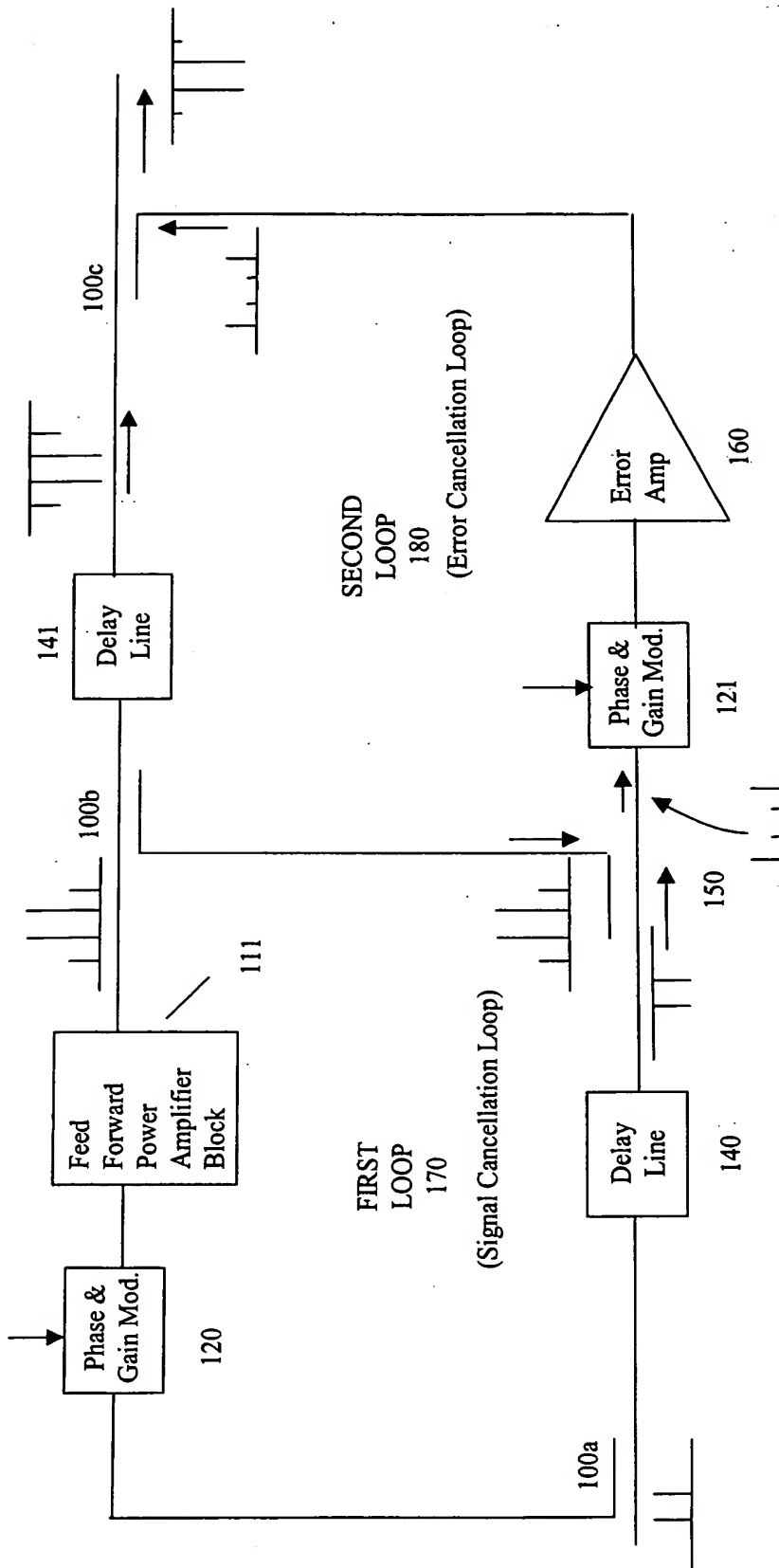
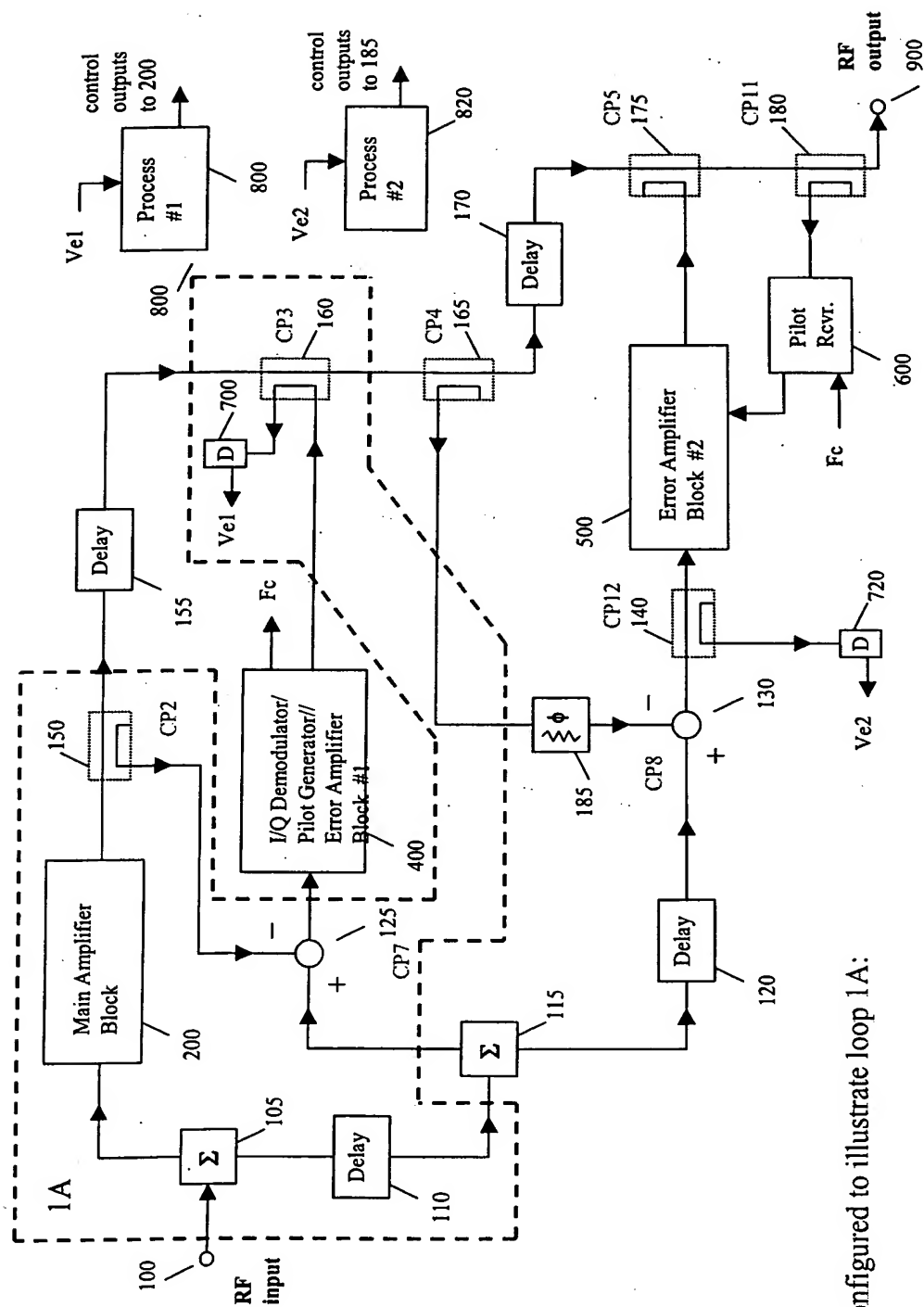


FIG. 2
PRIOR ART



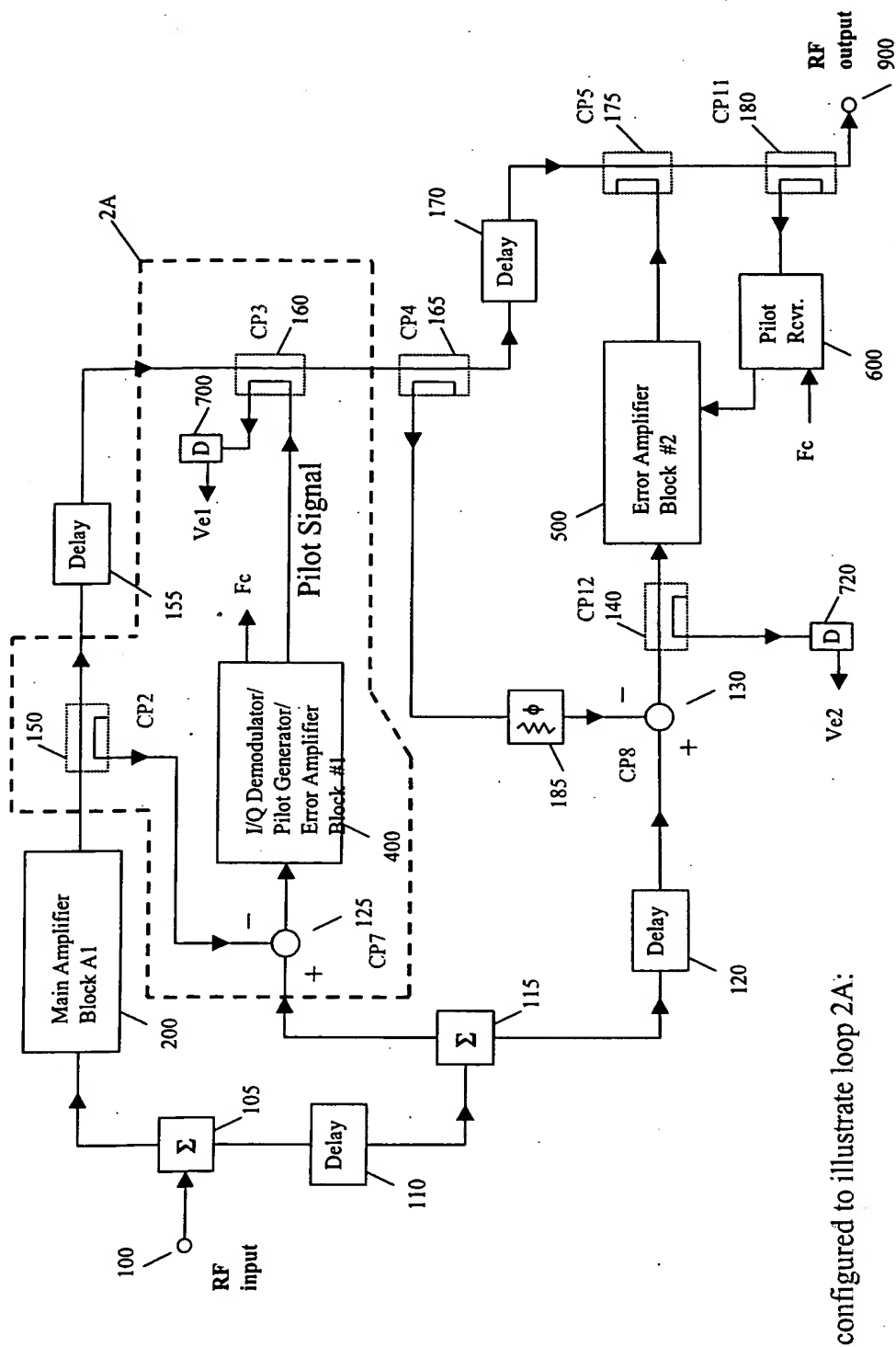
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FIG. 3



configured to illustrate loop 1A:

FIG. 4



configured to illustrate loop 2A:

FIG. 5



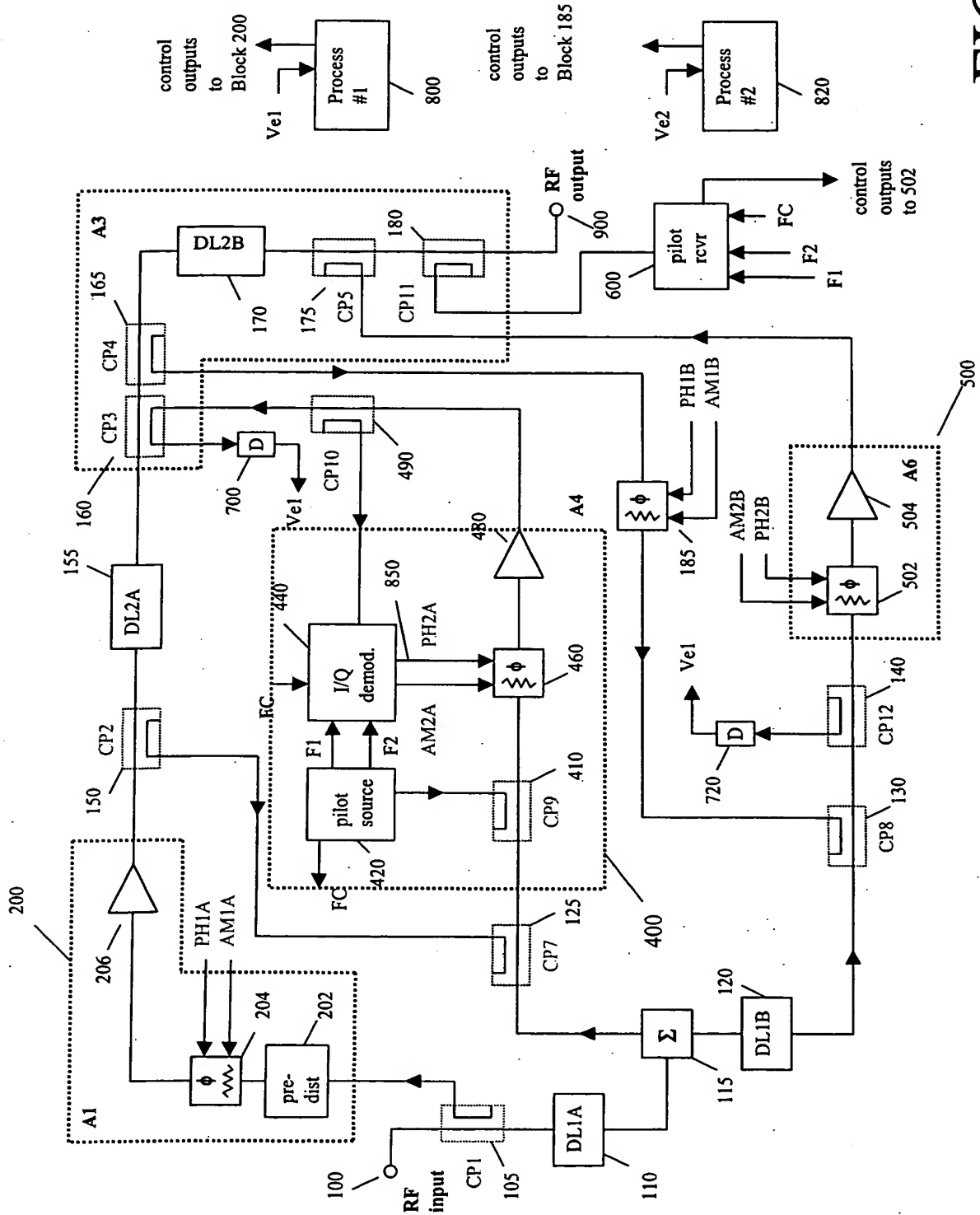


FIG. 8

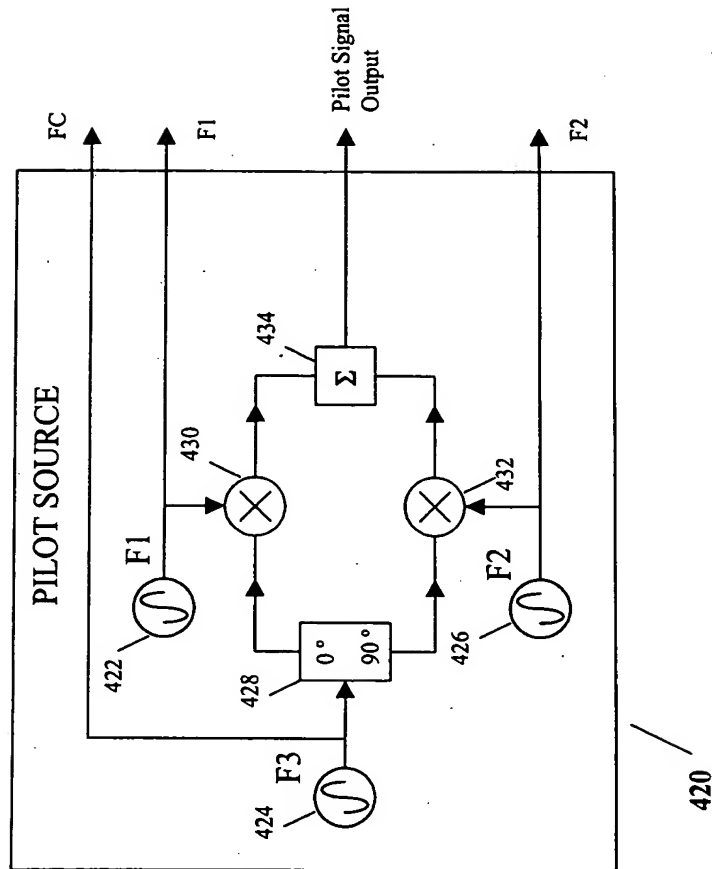


FIG. 9

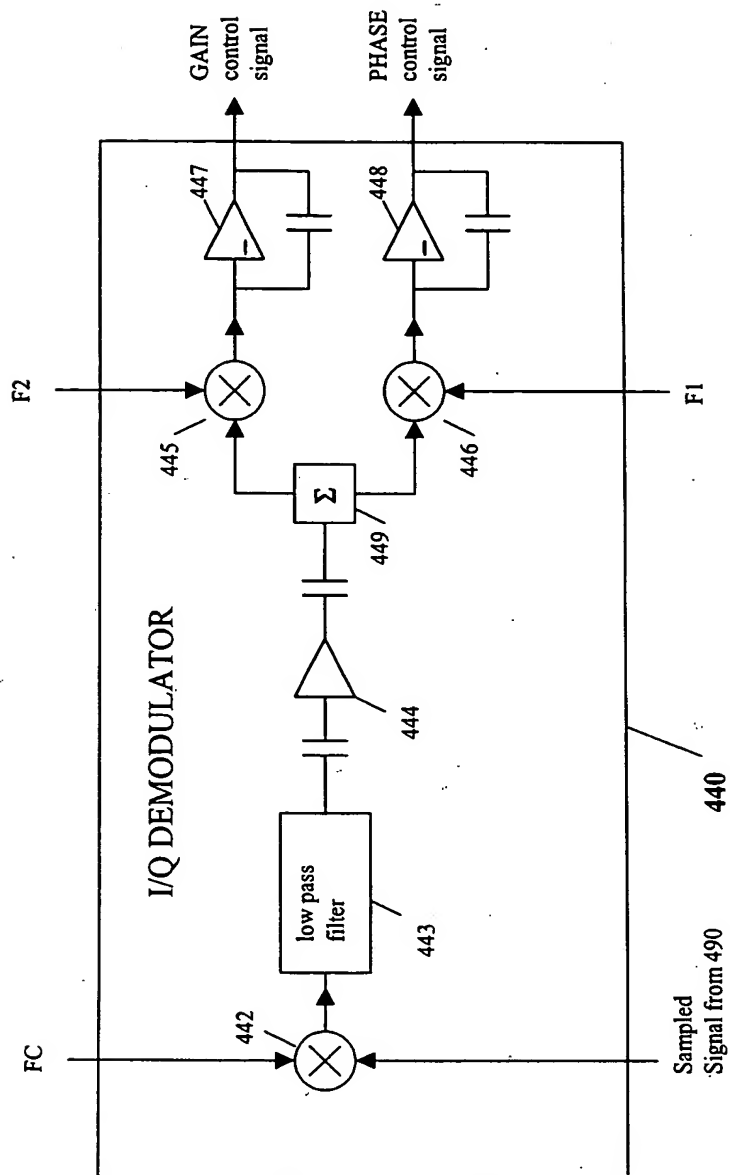
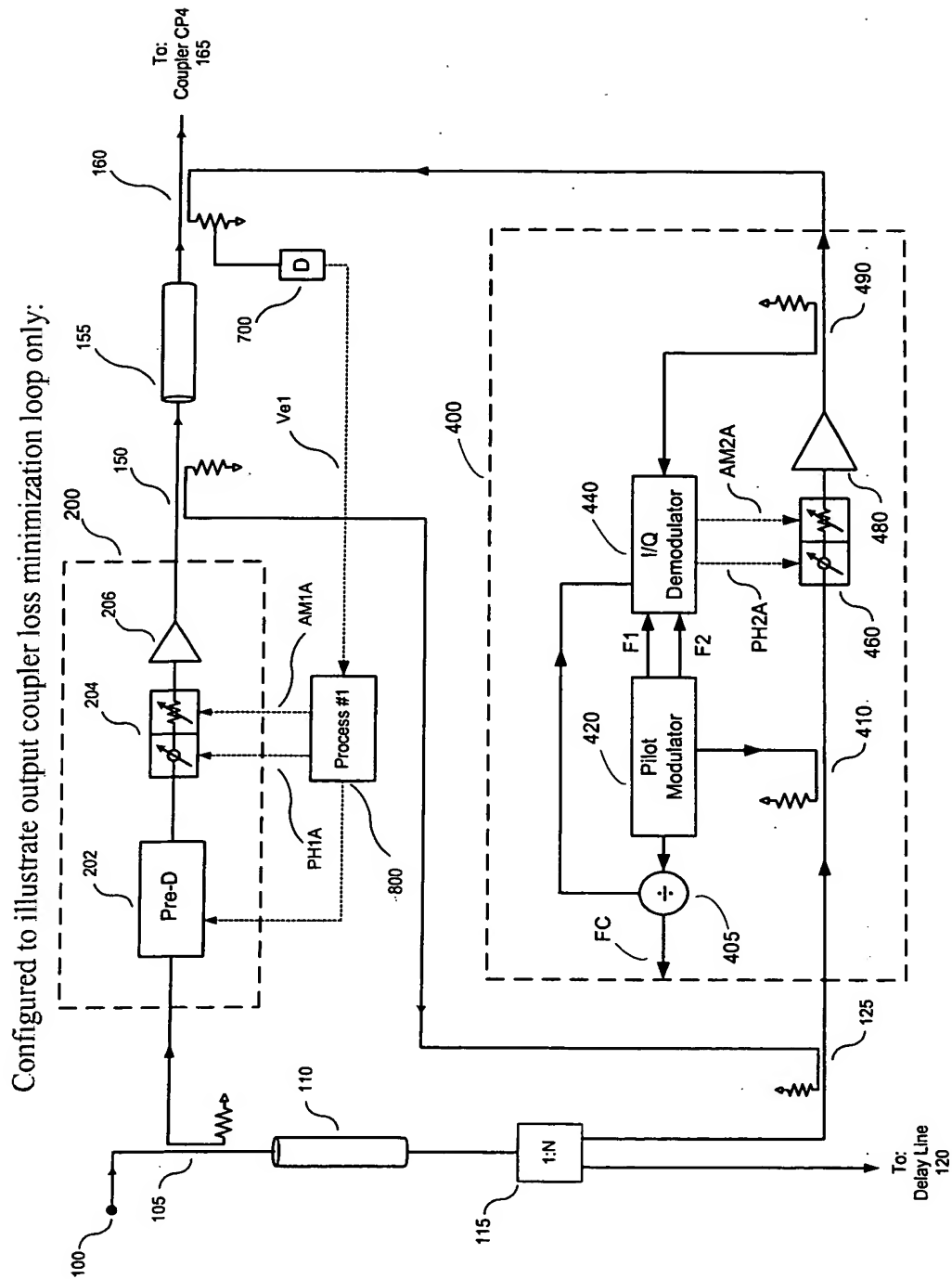


FIG. 10



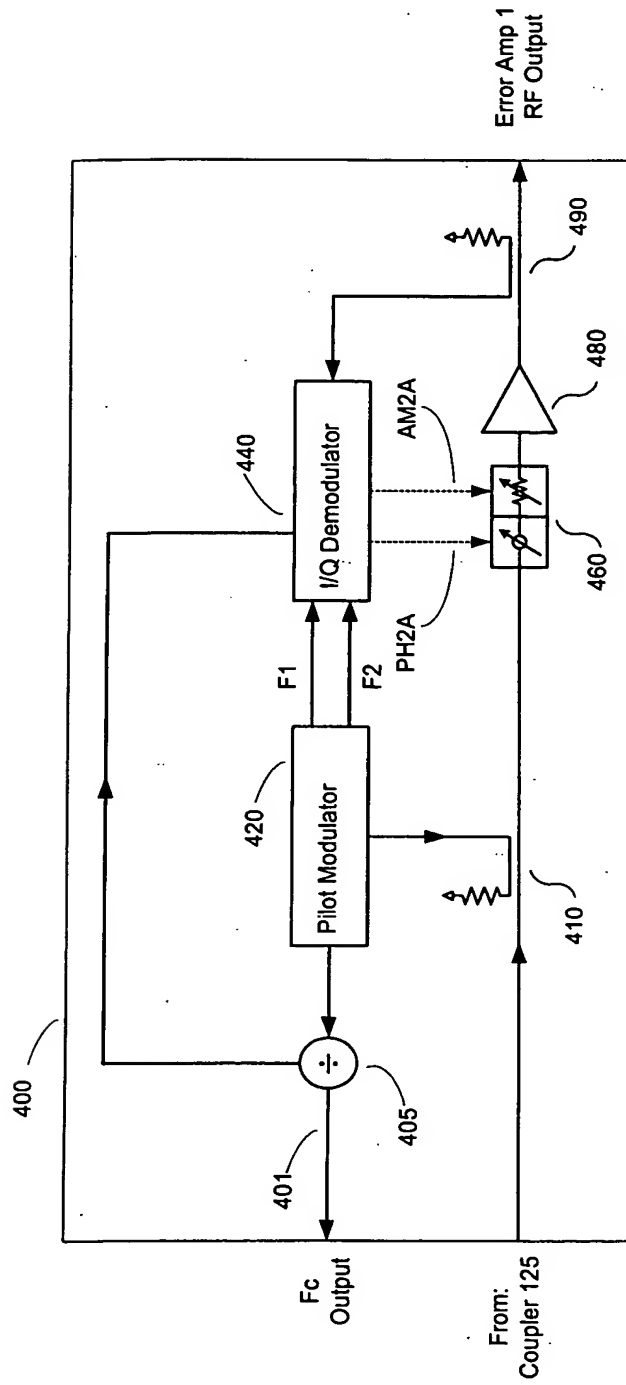


FIG. 12



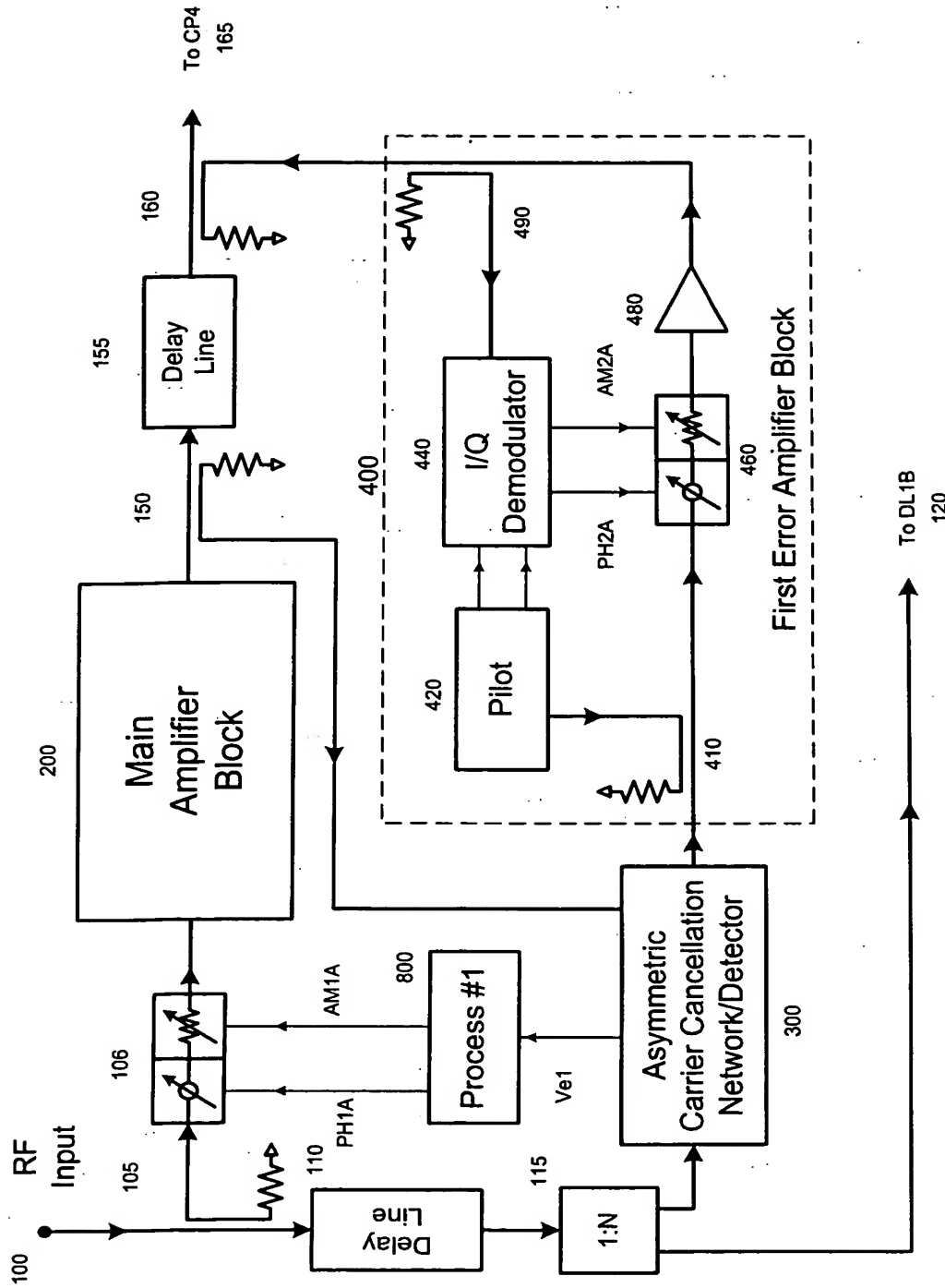


FIG. 13B

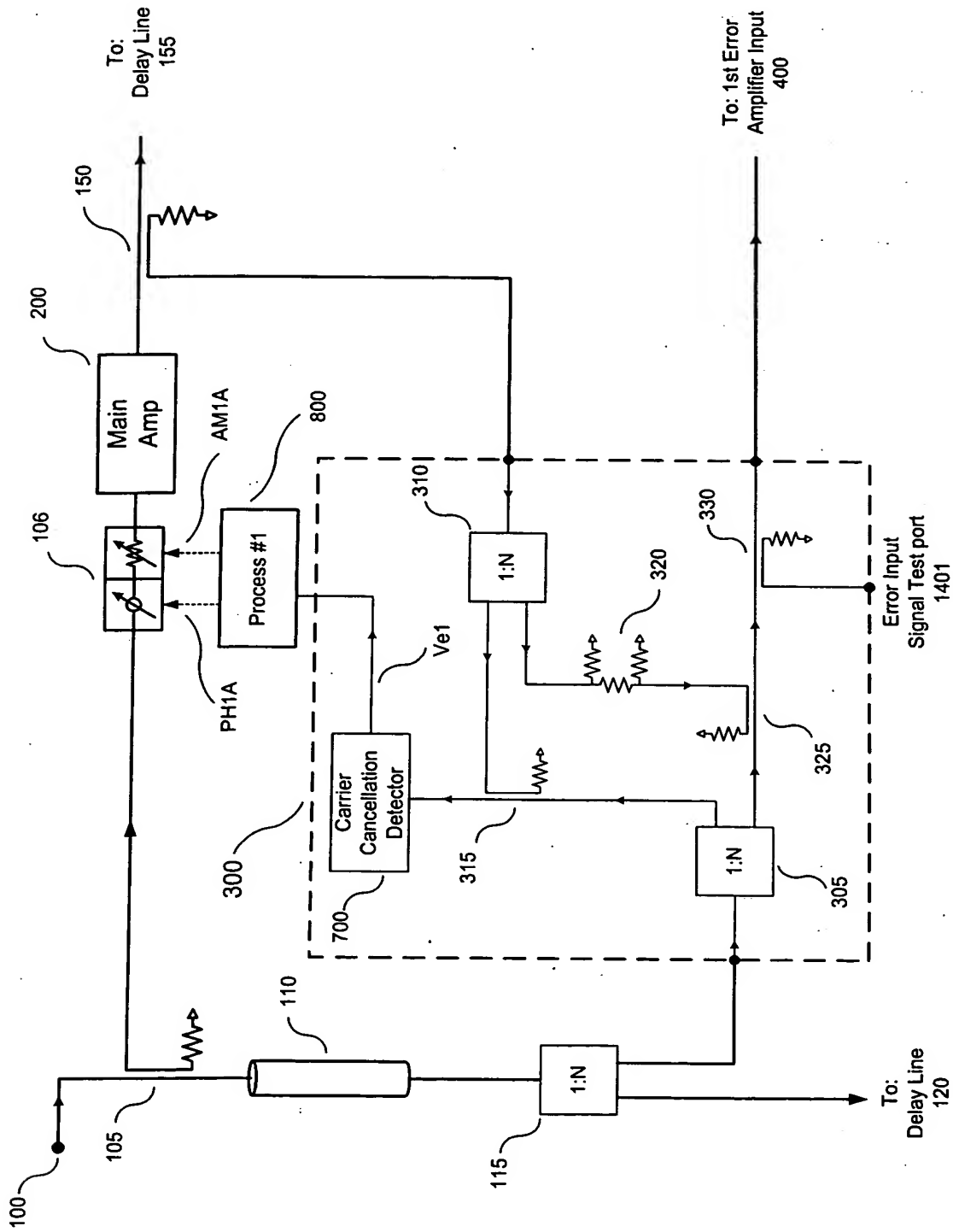


FIG. 14



Error Amplifier Stabilization

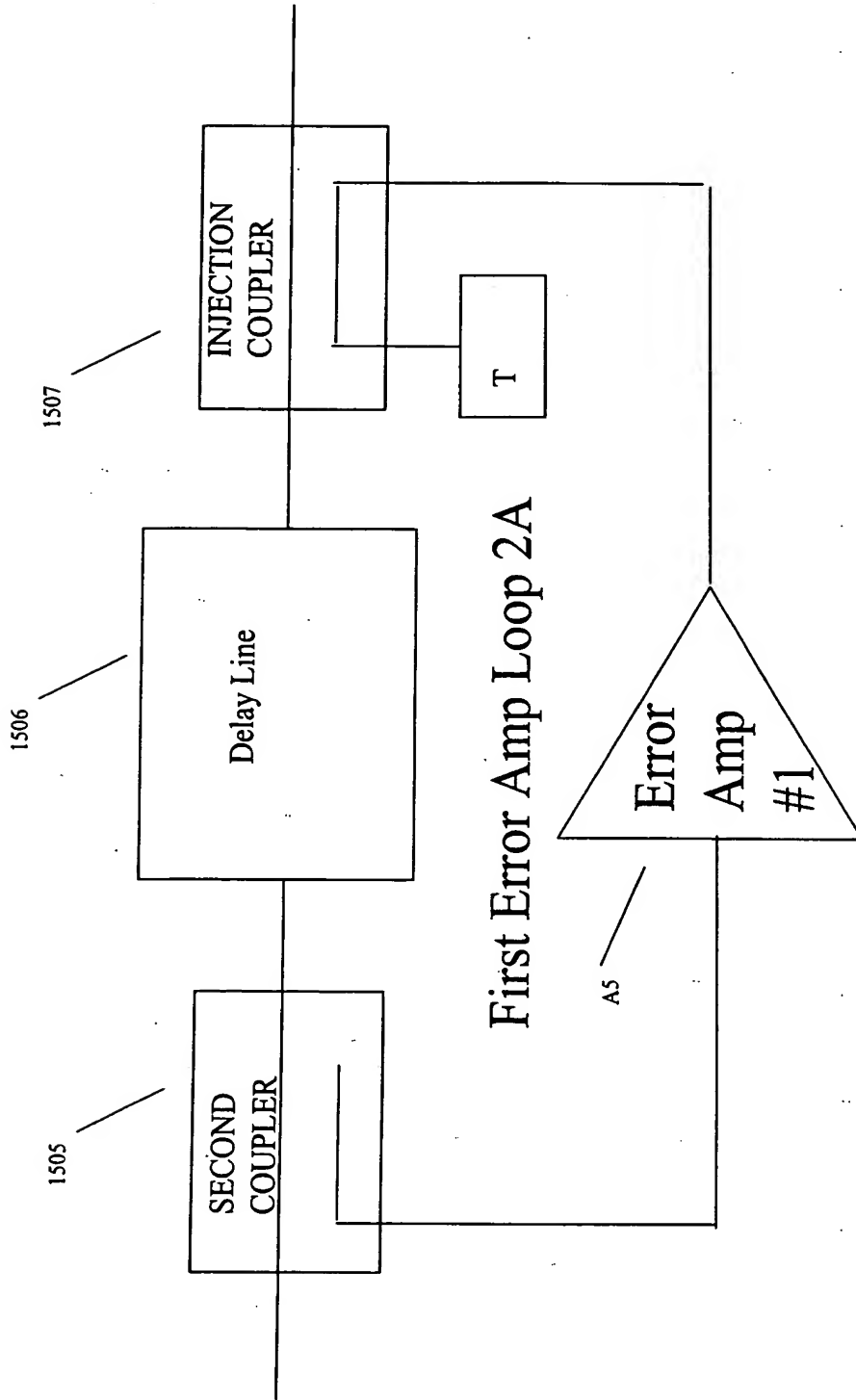


FIG. 16

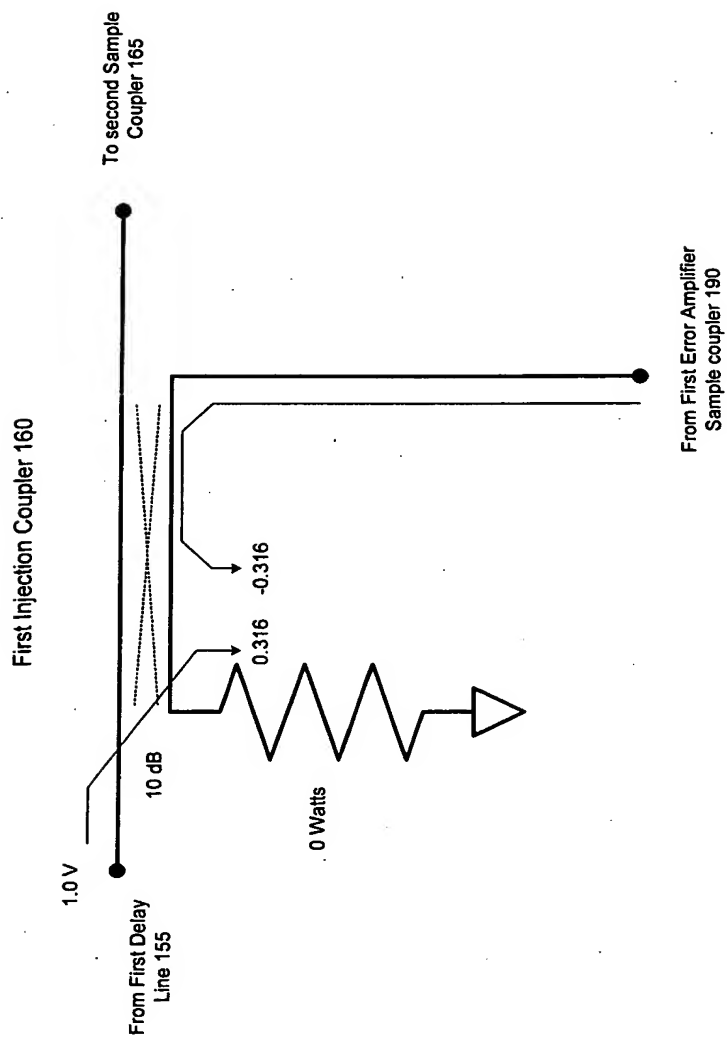


FIG. 17



Non-Overlapping Loop Adjustment

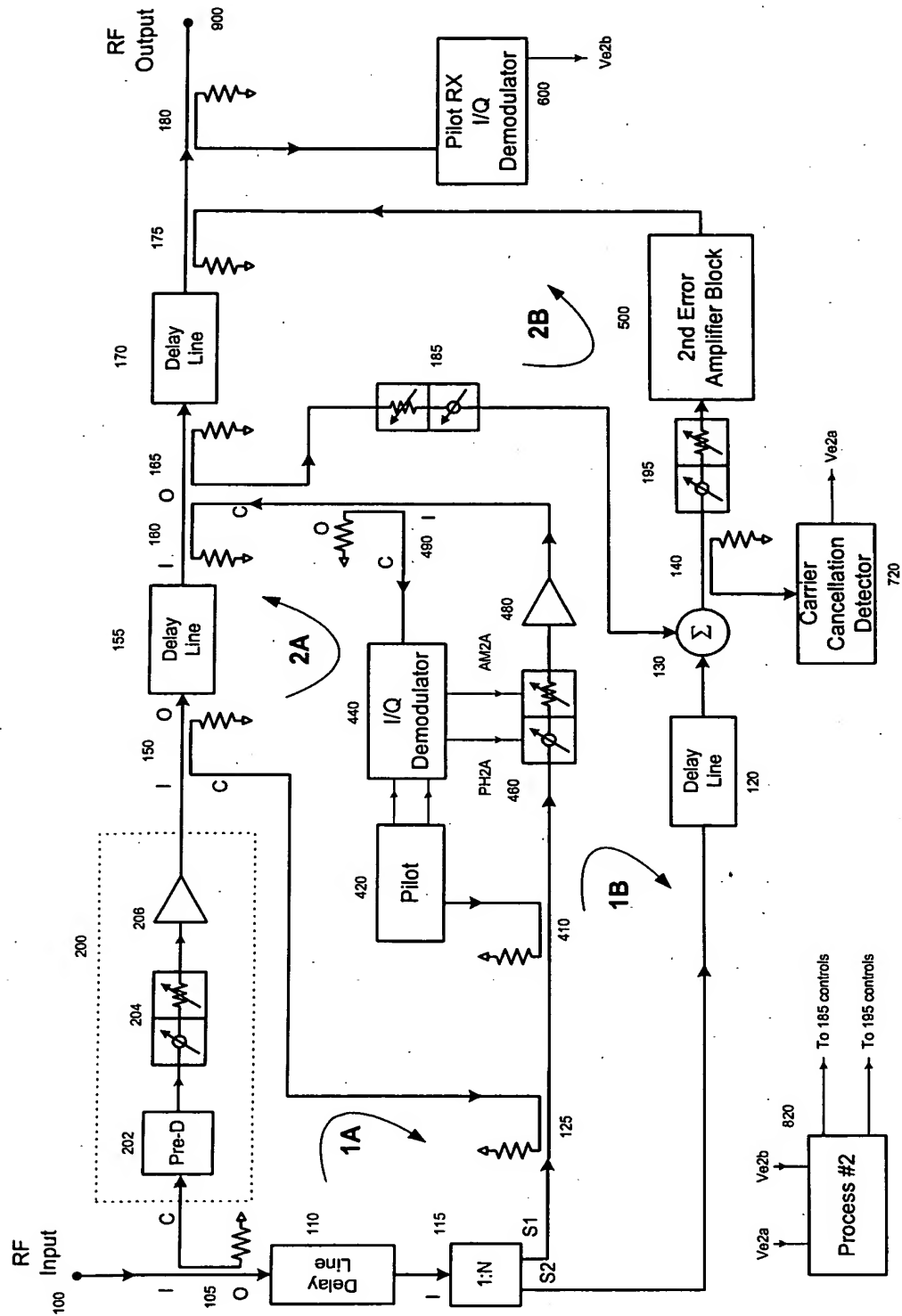


FIG. 19